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APPLICANT(S) Yang Du GROUP ART UNIT: 2812  
APPLN. NO.: 10/728,621 EXAMINER: H. Jey Tsai  
FILED: December 5, 2003  
TITLE: METHOD AND CIRCUIT FOR MULTIPLYING SIGNALS WITH A  
TRANSISTOR HAVING MORE THAN ONE INDEPENDENT GATE  
STRUCTURE

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AMENDMENT

Commissioner of Patents  
Alexandria, VA 22313

Sir:

Please enter this Preliminary Amendment in the above-entitled application.

Please enter the following amendments: